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所有發表期刊論文

PUBLICATION LIST

BOOKS

1. C. M. Tan, W. Li, Z. Gan, and Y. Hou. *Applications of finite element methods for reliability studies on ULSI Interconnections*. UK: Springer Verlag, 2011.
2. C. M. Tan. *Electromigration in ULSI Interconnections*. New York: World Scientific, 2010.
3. C. M. Tan. *Simulated Annealing*. Vienna, Austria: IN-TECH Publishing, 2008.
4. C. M. Tan and F. He. *Electromigration Modeling at Circuit Layout Level*. SpringerBrief on Reliability, 2013.

BOOK CHAPTERS

1. C. M. Tan. “Reliability Assessment of Integrated Circuits and its Misconception,” in *Integrated Circuits, Photodiodes and Organic Field Effect Transistors*. New York, United States: Nova Scientific Publishing, 2009, pp. 45-68.
2. C. M. Tan and N. Raghavan. “Simulated Annealing for Mixture Distribution Analysis and its Applications to Reliability Testing” in *Simulated Annealing*. Vienna, Austria: IN-TECH Publishing, 2008, pp.227-256.
3. Tan Cher Ming and Hou Yuejin. “Changing Reliability Physics of Interconnect from Micro- to Nanotechnology”, in *Ceramic Integration and Joining Technologies: From Macro to Nanoscale*, John Wiley, 2010
4. Cher Ming Tan, “Component Level Reliability: Physical models and testing regulations” in “Handbook of Advanced Lighting Technology”, Springer-Verlag GmbH, Germany, 2014.

JOURNAL PAPERS

Interconnects Reliability

1. Yizhen Tian, Feifei He, Qi-Jun Zhang, Cher Ming Tan, Jianguo Ma, “Rapid ULSI Interconnect Reliability Analysis Using Neural Networks” accepted in IEEE Trans. On Devices and Materials Reliability.

2. K. Ghosh, J. Zhang, L. Zhang, Y. Dong, H. Li, C. M. Tan, G. Xia, and C. S. Tan. "Integration of Low-k Dielectric Liner in Through Silicon Via and Thermomechanical Stress Relief," *Physics Express*, vol 5(12), Dec 2012
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6. C. M. Tan, W. Li, and Z. Gan. "Applications of Finite element Methods for Reliability Study of ULSI Interconnections," *Microelectronics Reliability*, vol. 52, no. 8, pp. 1539-1545, Aug. 2012.
7. Y. D. Han, S. M. L. Nai, H. Y. Jing, L. Y. Xu, C. M. Tan, and J. Wei. "Development of a Sn-Ag-Cu solder reinforced with Ni-coated carbon nanotubes," *Journal of Materials Science-Materials in Electronics*, vol. 22, pp. 315-322, 2011.
8. Y. C. Tan, C. M. Tan, X. W. Zhang, T. C. Chai, and D. Q. Yu. "Electromigration performance of through silicon via (TSV) - A modeling approach," *Microelectronics Reliability*, vol. 50, no. 9, pp. 1336-1340, 2010.
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Wafer Bonding

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Integrated Circuit Reliability

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LED Reliability

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