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所有發表期刊論文

Journal Papers:

1. **Yuan-Ho Chen**, Chung-Yi Li* and Lu-An Lai, "Fine-tuning accuracy using conditional probability of the bottom sign-bit in Fixed-width Modified Booth Multiplier," *Circuits Syst. Signal Process.*, 2017. (SCI)
2. Yun-Hua Tseng, **Yuan-Ho Chen*** and Chih-Wen Lu*, "Adaptive Integration of the Compressed Algorithm of CS and NPC for the ECG Signal Compressed Algorithm in VLSI Implementation," *Sensors*.vol.17, pp. 2288, Oct. 2017.(SCI)
3. **Yuan-Ho Chen*** and Yi-Fan Ko, "High Throughput Video Transform Architecture for High Efficiency Video Coding (HEVC)," *International Journal of Circuit Theory and Applications*, vol. 45, issue 12, pp. 2260-2269, Dec. 2017.(SCI)
4. **Yuan-Ho Chen***, "A counting-weighted calibration method for a field-programmable-gate-array-based time-to-digital converter," *Nuclear Instruments and Methods in Physics Research Section A*, vol. 854, pp. 61-63, Feb. 2017. (SCI)
5. **Yuan-Ho Chen*** and Yun-Hua Tseng, "Low-cost Multi-standard Video Transform Core Using Time-distribution Scheme," *Electron. Lett.*, vol. 52, issue 24, pp. 1980-1982, Nov. 2016.(SCI)
6. Yun-Hua Tseng, **Yuan-Ho Chen***, Tze-Yang Kao, and Chih-Wen Lu, "Low-cost Multi-Standard Simultaneous Forward and Inverse Video Transform Core," *International Journal of Circuit Theory and Applications*, vol. 44, issue 8, pp. 1572-1588, Aug. 2016. (SCI)
7. Wen-Quan He, **Yuan-Ho Chen***, and Shyh-Jye Jou, "Dynamic Error-compensated Fixed-Width Booth Multiplier Based on Conditional-Probability of Input Series," *Circuits Syst. Signal Process.*, vol. 35, no. 8, pp. 2972-2991, Aug. 2016.(SCI)
8. Ping-Yeh Yin, Chih-Wen Lu*, **Yuan-Ho Chen**, Hsin-Chin Liang, and Sheng-Pin Tseng "A 10-Bit Low-Power High-Color-Depth Column Driver with Two-Stage Multi-Channel RDACs for Small-Format TFT-LCD Driver ICs," *IEEE Journal of Display Technology*, vol. 11, no. 12, pp. 1061-1068, Dec. 2015.(SCI)

9. Szi-Wen Chen* and **Yuan-Ho Chen**, "Hardware Design and Implementation of a Wavelet De-noising Procedure for Medical Signal Preprocessing," *Sensors*, vol. 15, pp. 26396-26414, Oct. 2015. (SCI)
10. **Yuan-Ho Chen***, "Area-Efficient Fixed-Width Squarer with Dynamic Error-Compensation Circuit," *IEEE Trans. Circuits Syst. II.*, vol. 62, no. 9, pp. 851-855, Sep. 2015. (SCI)
11. Wen-Quan He, **Yuan-Ho Chen***, and Shyh-Jye Jou, "High-Accuracy Fixed-Width Booth Multipliers Based on Probability and Simulation," *IEEE Trans. Circuits Syst. I.*, vol. 62, no. 8, pp. 2052-2061, Aug. 2015. (SCI)
12. **Yuan-Ho Chen*** and Chieh-Yang Liu, "Area-efficient Video Transform for HEVC Applications," *Electron. Lett.*, vol. 51, no. 14, pp. 1065-1067, Jul, 2015. (SCI)
13. **Yuan-Ho Chen***, "An Accuracy-Adjustment Fixed-Width Booth Multiplier Based on Multilevel Conditional Probability," *IEEE Trans. Very Large Scale Integr. (VLSI) Syst.*, vol. 23, no. 1, pp. 203-207, Jan. 2015. (SCI)
14. **Yuan-Ho Chen***, Ruei-Yuan Jou, Tsin-Yuan Chang, and Chih-Wen Lu, "A High-Throughput and Area-Efficient Video Transform Core with a Time Division Strategy," *IEEE Trans. Very Large Scale Integr. (VLSI) Syst.*, vol. 22, no. 11, pp. 2268-2277, Nov. 2014. (SCI)
15. **Yuan-Ho Chen*** and Hsiao-Tzu Liu, "Hardware-Efficient Multi-Standard Video Transform Core," *J. Circuits Syst. Comput.*, vol. 23, no. 8, pp. 1450119, 2014. (SCI)
16. **Yuan-Ho Chen***, "Low-cost fixed-width squarer by using a probability-compensated circuit," *Electron. Lett.*, vol. 50, no. 11, pp. 795-797, May 2014. (SCI)
17. **Yuan-Ho Chen***, Chih-Wen Lu, Shian-Shing Shyu, Chung-Lin Lee, and Ting-Chia Ou, "A Multi-stage Fault-tolerant Multiplier with Triple Module Redundancy (TMR) Technique," *J. Circuits Syst. Comput.*, vol. 23, no. 5, pp. 1450074, 2014. (SCI)
18. Wen-Quan He, Yong-Ming Chang, and **Yuan-Ho Chen***, "High-Throughput Rate FFT VLSI Implementation on Linear Array Based Design," *Journal of Advanced Engineering*, vol. 9, no. 2, pp. 87-92, Apr. 2014.

19. **Yuan-Ho Chen***, Jyun-Neng Chen, Tsin-Yuan Chang, and Chih-Wen Lu, "High-Throughput Multistandard Transform Core Supporting MPEG/H.264/VC-1 Using Common Sharing Distributed Arithmetic," *IEEE Trans. Very Large Scale Integr. (VLSI) Syst.*, vol. 22, no. 3, pp. 463-474, Mar. 2014. (SCI)
20. **Yuan-Ho Chen*** and Tsin-Yuan Chang, "A High Performance Video Transform Engine by Using Space-Time Scheduling Strategy," *IEEE Trans. Very Large Scale Integr. (VLSI) Syst.*, vol. 20, no. 4, pp. 655-664, Apr. 2012.(SCI)
21. **Yuan-Ho Chen***and Tsin-Yuan Chang, "A High-Accuracy Adaptive Conditional-Probability Estimator for Fixed-Width Booth Multipliers," *IEEE Trans. Circuits Syst. I*, vol. 59, no. 3, pp. 594-603, Mar. 2012.(SCI)
22. Chung-Yi Li*, **Yuan-Ho Chen**, Tsin-Yuan Chang, Lih-Yuan Deng, and Kiwing To, "Period Extension and Randomness Enhancement Using High-Throughput Reseeding-Mixing PRNG," *IEEE Trans. Very Large Scale Integr. (VLSI) Syst.*, vol. 20, no. 2, pp. 385-389, Feb. 2012.(SCI)
23. **Yuan-Ho Chen***, Tsin-Yuan Chang, and Chung-Yi Li, "Area-Effective and Power-Efficient Fixed-Width Booth Multipliers Using Generalized Probabilistic Estimation Bias," *IEEE J. Emerging Sel. Topics Circuits Syst.*, vol. 1, no. 3, pp. 277-288, Sep. 2011.(SCI)
24. Chung-Yi Li*, **Yuan-Ho Chen**, Tsin-Yuan Chang, and Jyun-Neng Chen, "A Probabilistic Estimation Bias Circuit for Fixed-Width Booth Multiplier and Its DCT Applications," *IEEE Trans. Circuits Syst. II*, vol. 58, no. 4, pp. 215-219, Apr. 2011. (SCI)
25. **Yuan-Ho Chen***, Tsin-Yuan Chang, and Chung-Yi Li, "High Throughput DA-based DCT with High Accuracy Error-Compensated Adder Tree," *IEEE Trans. Very Large Scale Integr. (VLSI) Syst.*, vol. 19, no. 4, pp. 709-714, Apr. 2011. (SCI)
26. Bor-Sen Chen*, Bore-Kuen Lee, and **Yuan-Ho Chen**, "Power Control for CDMA Cellular Radio Systems via l_1 Optimal Predictor," *IEEE Trans. Wireless Commun.*, vol. 5, no. 11, pp. 2914-2922, Oct. 2006. (SCI)

27. Bore-Kuen Lee, Yuan-Ho Chen, and Bor-Sen Chen*, "Robust H_∞ Power Control for CDMA Cellular Communication Systems," *IEEE Trans. Signal Processing*, vol. 54, no. 10, pp. 3947-3956, Oct. 2006. (SCI)