

\*周煌程副教授

所有發表期刊論文

1. I-C. Wey, C.-C. Peng and H.-C. Chow, "Wide Bandwidth and High Precision Power Supply Noise Detector by Using Dual Peak Detection Sample and Hold Circuits", International Journal of Circuit Theory and Applications, Vol. 42, pp. 529-541, May 2014.
2. I-C. Wey, T.-C. He, H.-C. Chow, P.-H. Sun and C.-C. Peng , "A High-Speed, High Fan-In Dynamic Comparator with Low Transistor Count", International Journal of Electronics, Vol. 101, pp. 681-690, May 2014.
3. H.-C. Chow and B.-S. Tang, "A High Performance Current-Mode Instrumentation Amplifier With Level Shifter For Single Power Biomedical Applications", Applied Mechanics and Materials, Vol. 530, pp. 217-220, Feb. 2014.
4. H.-C. Chow and W.-T. Lin, "High Resolution Successive Approximation ADC for Low Power Biomedical Applications ", *Procedia Engineering*, Vol. 50 (10), pp. 275-283, Oct. 2012.
5. W.-S. Feng, T. Wang, H.-C. Chow, H.-H. Li, and C.-M. Tsao , "VCO with magnetic flux control loop", *Electronics Letters*, Vol. 48 (14), pp. 859-861, July 2012.
6. H.-C. Chow and J.-H. Tien, "A High CMRR, Low Power Current-Mode Instrumentation Amplifier for Biomedical Applications", *Sensor Letters*, Vol. 10 , pp. 1149-1156, June 2012.
7. H.-C. Chow and J.-H. Huang, "A Pulse-Width Control Loop for Power Electronics Applications", *Energy Procedia*, Vol. 16 (1), 1247-1252, Jan. 2012.
8. H.-C. Chow and C.-L. Hsieh, "High speed charge transfer sense amplifier for 0.5V DRAM array applications", International Journal of Electronics, pp. 165-171, Vol. 96 (2), Feb. 2009.
9. H.-C. Chow and C.-H. Su, "A new all-digital phase-locked loop with high precision and low jitter", International Journal of Electronics, Vol. 95 (12), pp. 1241-1249, Dec. 2008.